



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kirk Martin  
Assignee: Nisene Technology Group  
Title: Method and Apparatus for Etching a Semiconductor Die  
Serial No.: 09/902,931 Filed: July 10, 2001  
Examiner: Vinh, Lan Group Art Unit: 1765  
Docket No.: M-11706 US  
(NTG-P101)

San Jose, California  
February 10, 2004

Mail Stop Issue Fee  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicant submits six (6) sheets of formal drawing, consisting of Figures 1, 2, 3, 4, 5, 6, 7, and 8, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (408) 382-0480.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on Feb. 10, 2004.

Carmen C. Cook  
\_\_\_\_\_  
Attorney for Applicant(s)

2/10/2004  
\_\_\_\_\_  
Date of Signature

Respectfully submitted,

Carmen C. Cook  
\_\_\_\_\_  
Carmen C. Cook  
Attorney for Applicant(s)  
Reg. No. 42,433